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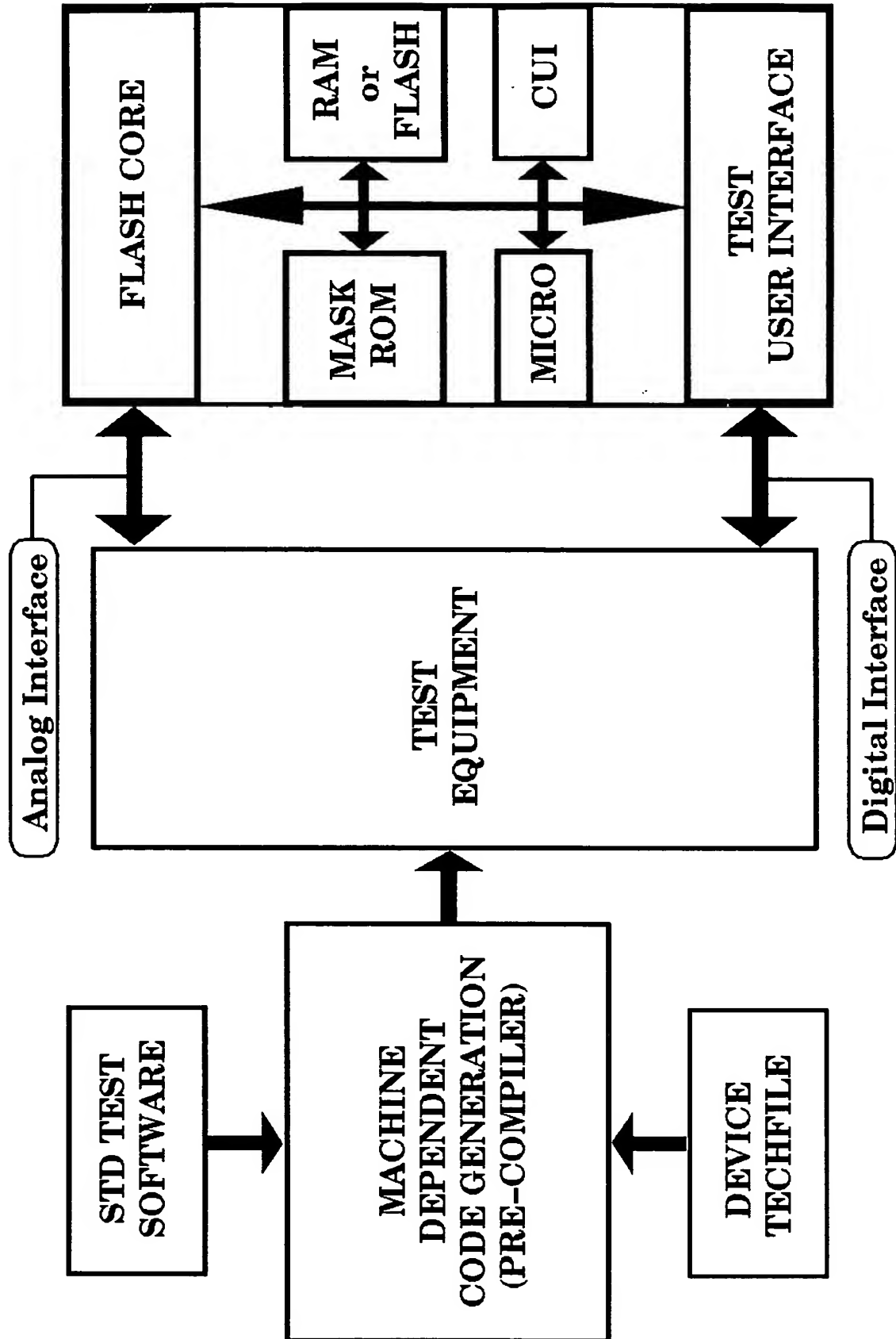


FIG. 1

Architecture (Part-1)

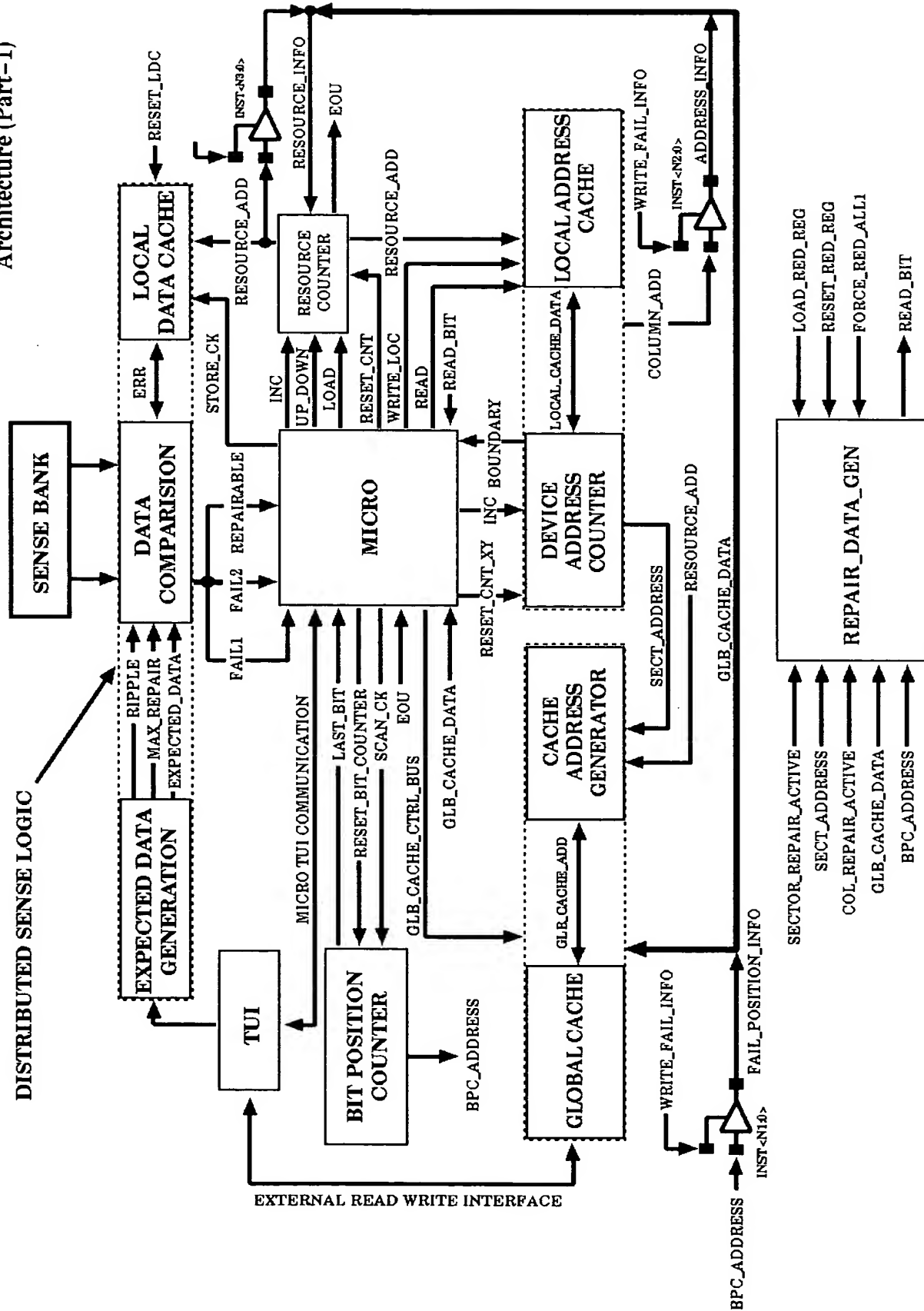


FIG. 2

Architecture (Part-2)

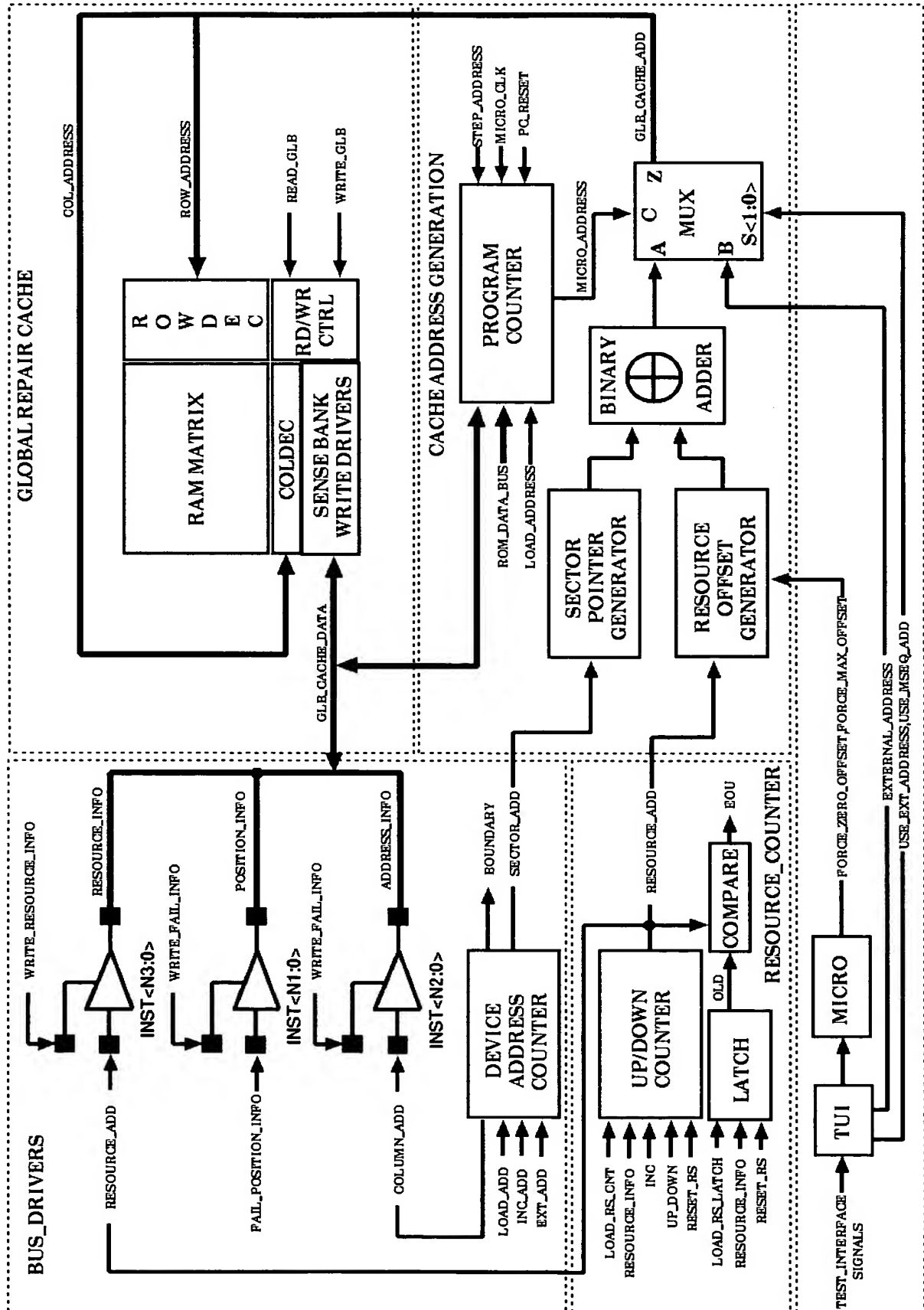


FIG. 3

Architecture (Part-3)

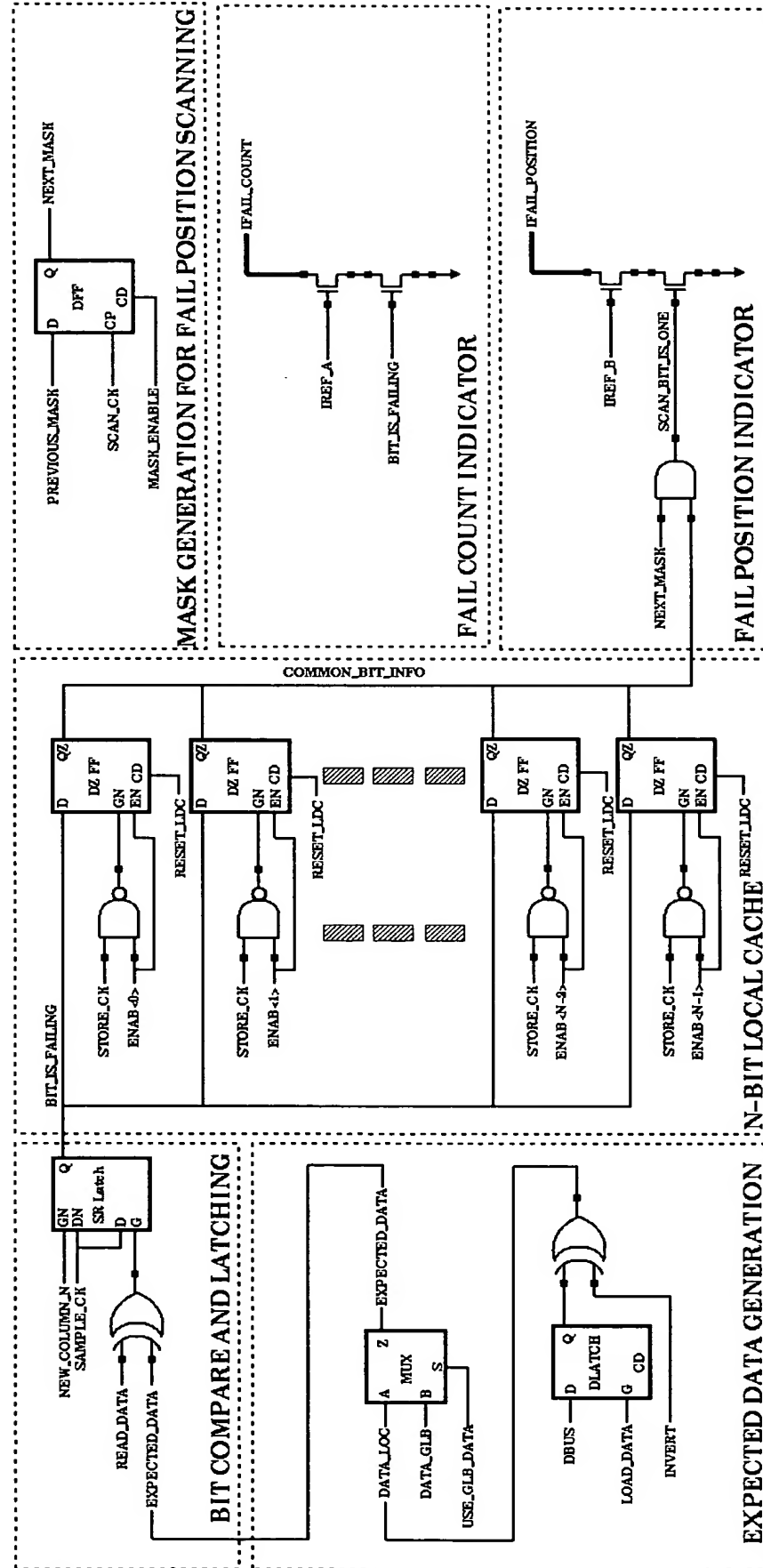


FIG. 4

Architecture (Part-4)

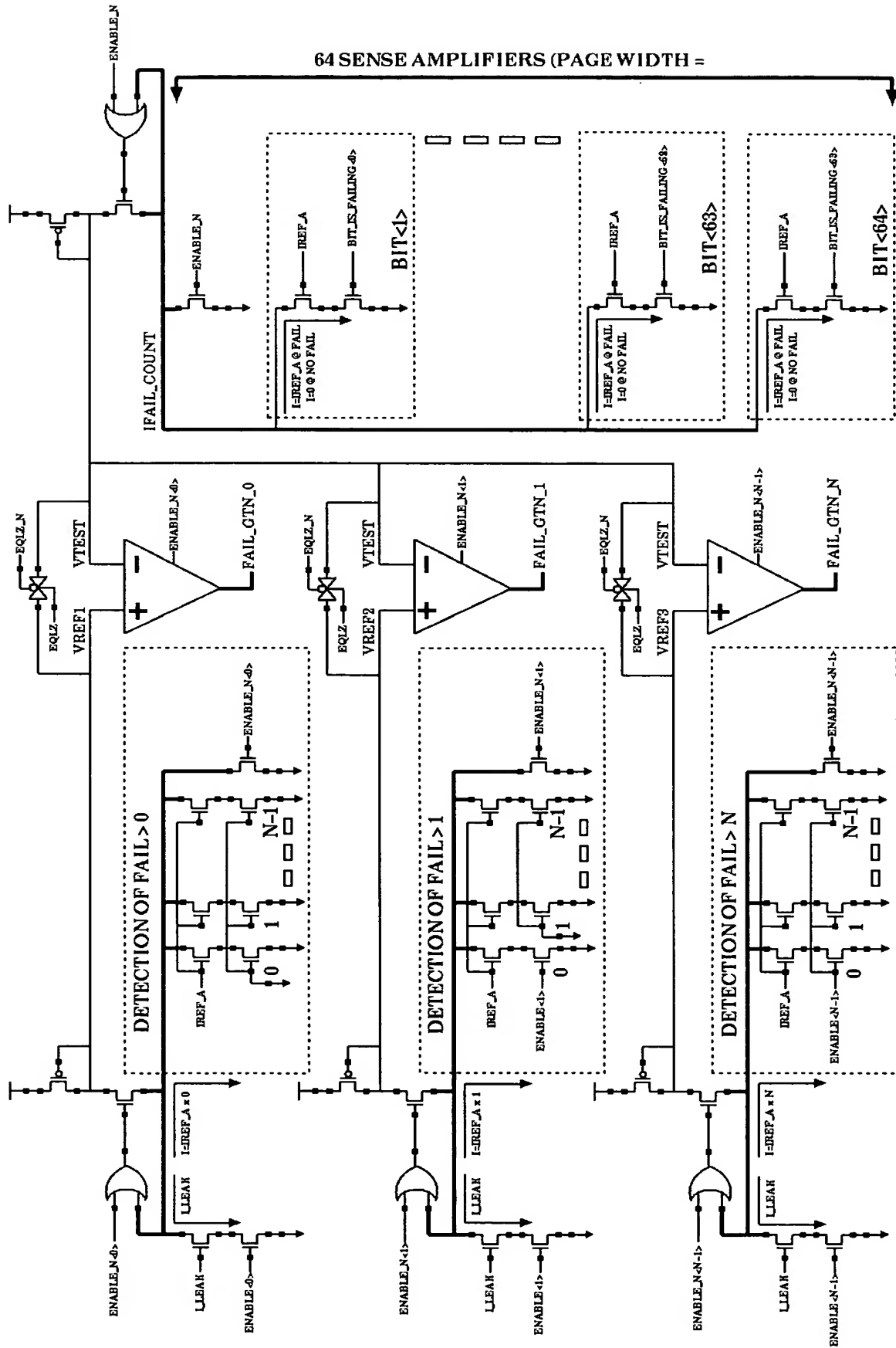


FIG. 5

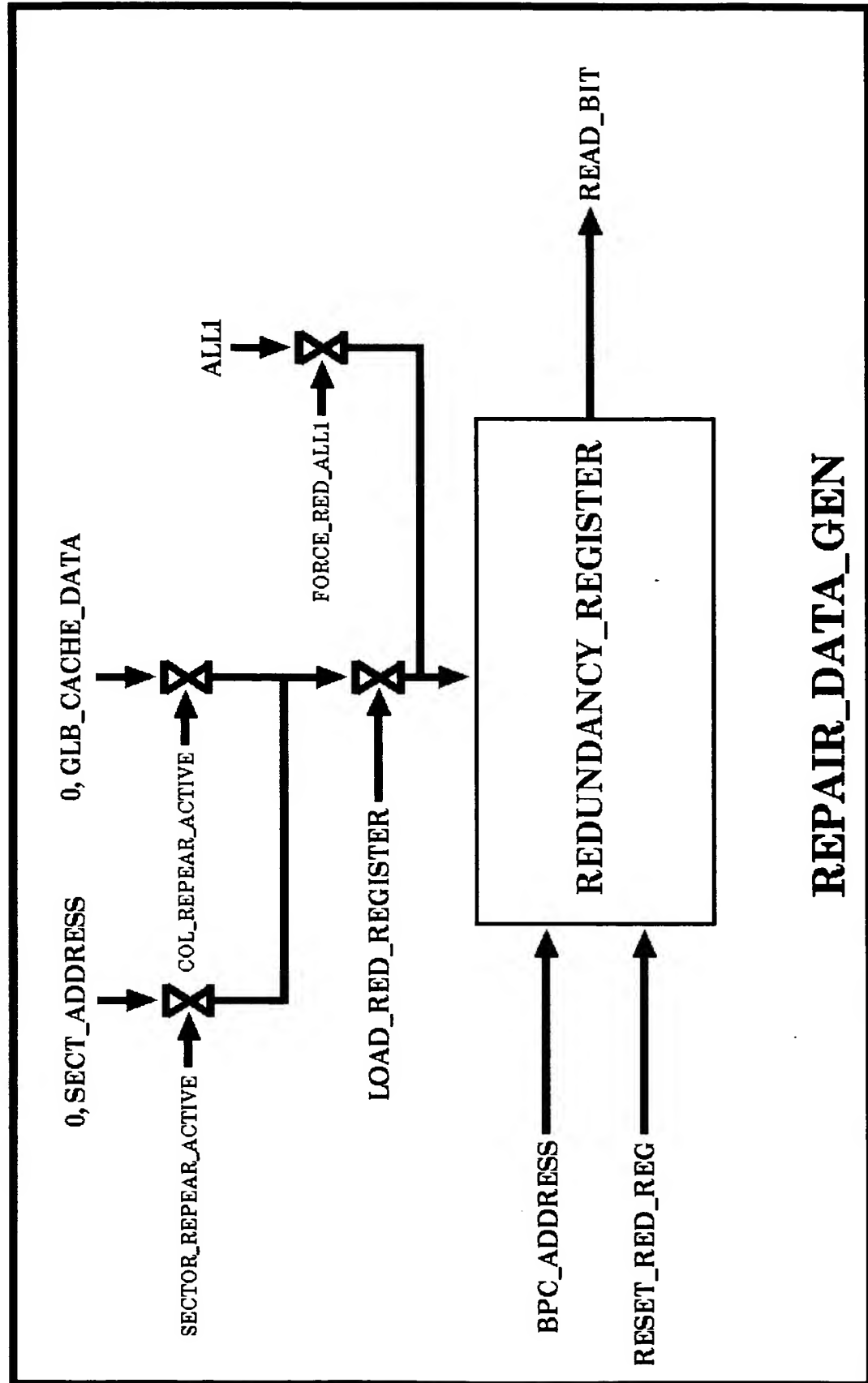


FIG. 6

Algorithm for Column & Sector
Redundancy Analysis (Part-1)

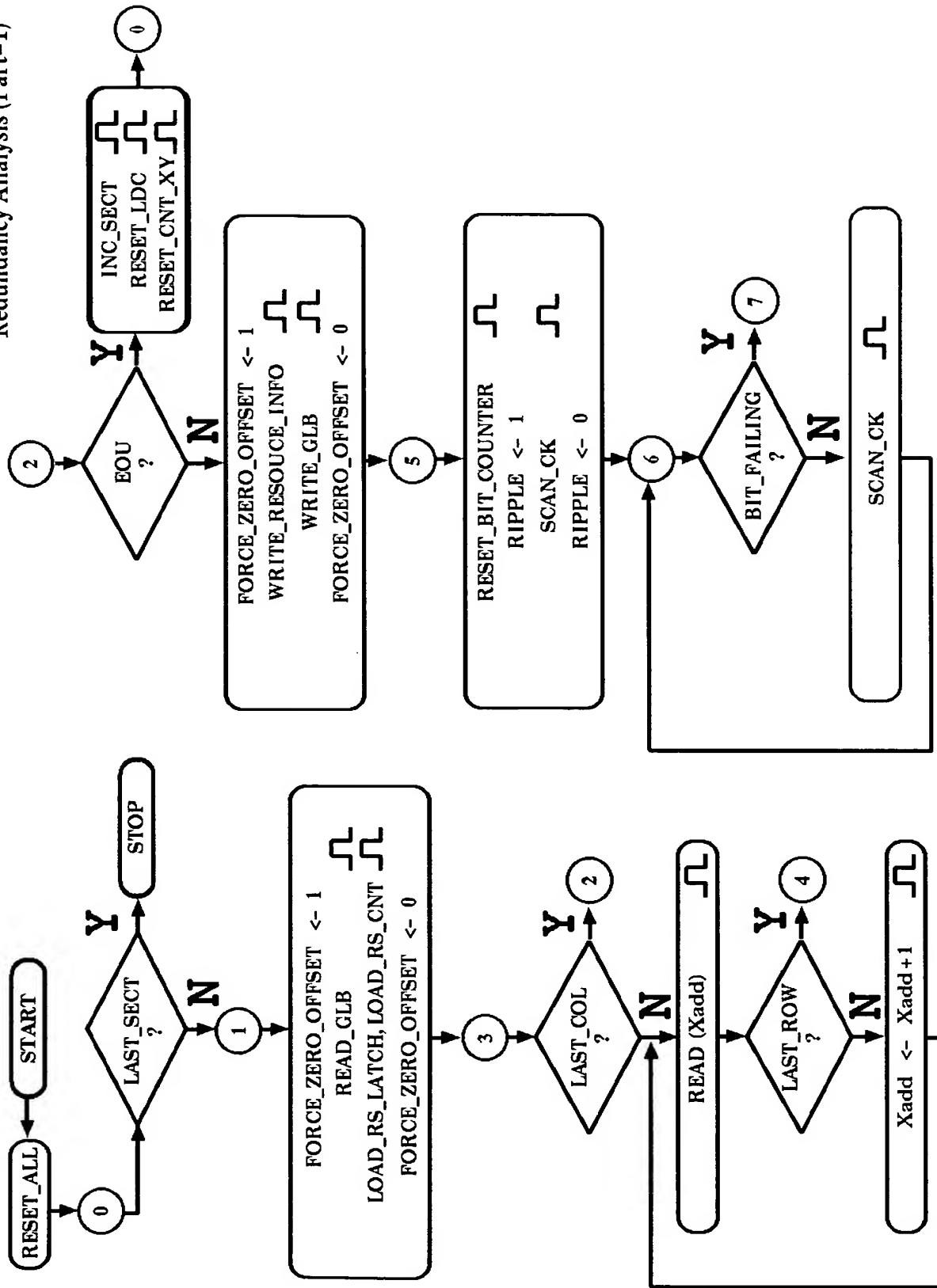


FIG. 7A

Algorithm for Column & Sector
Redundancy Analysis (Part-2)

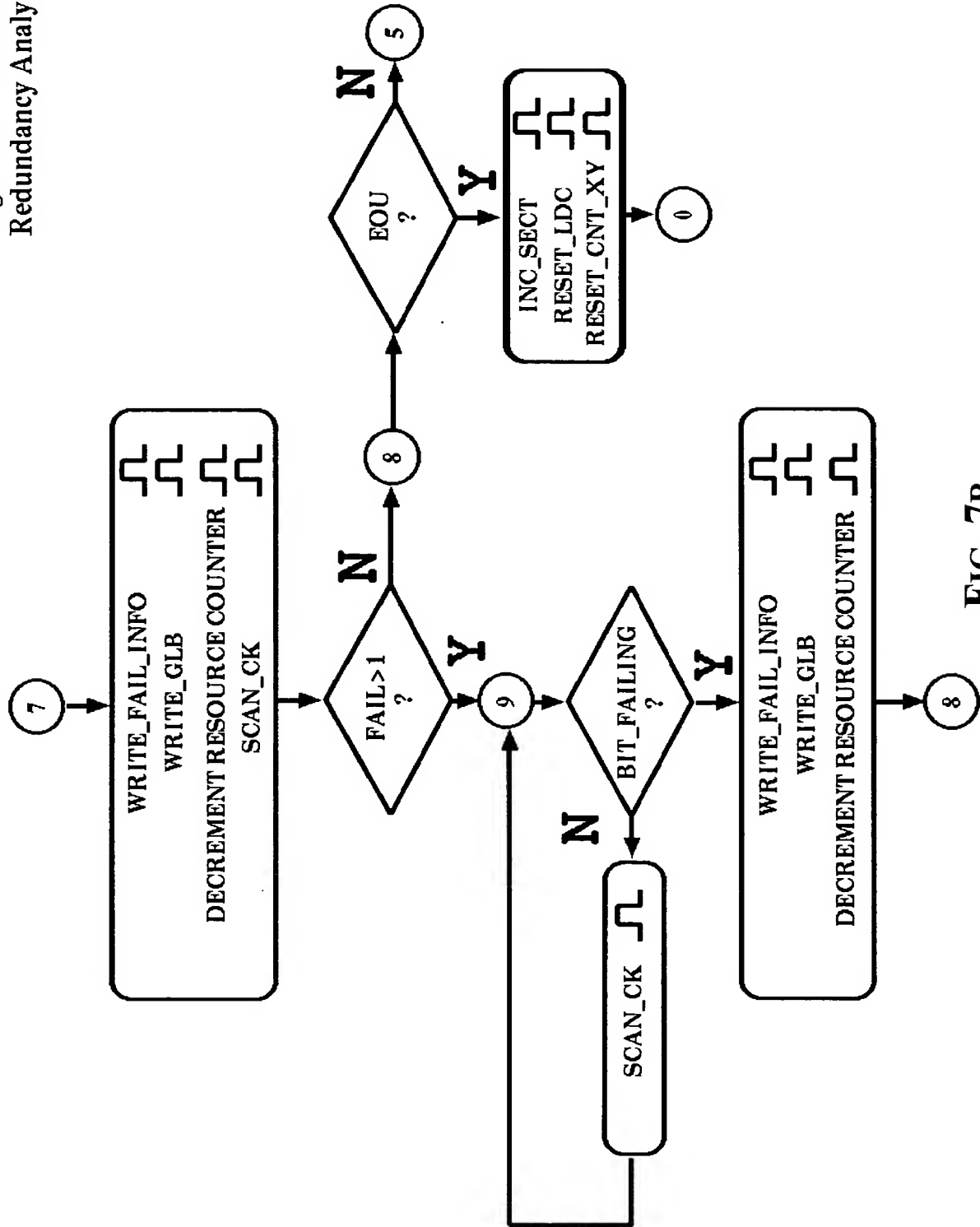


FIG. 7B

Algorithm for Column & Sector
Redundancy Analysis (Part-3)

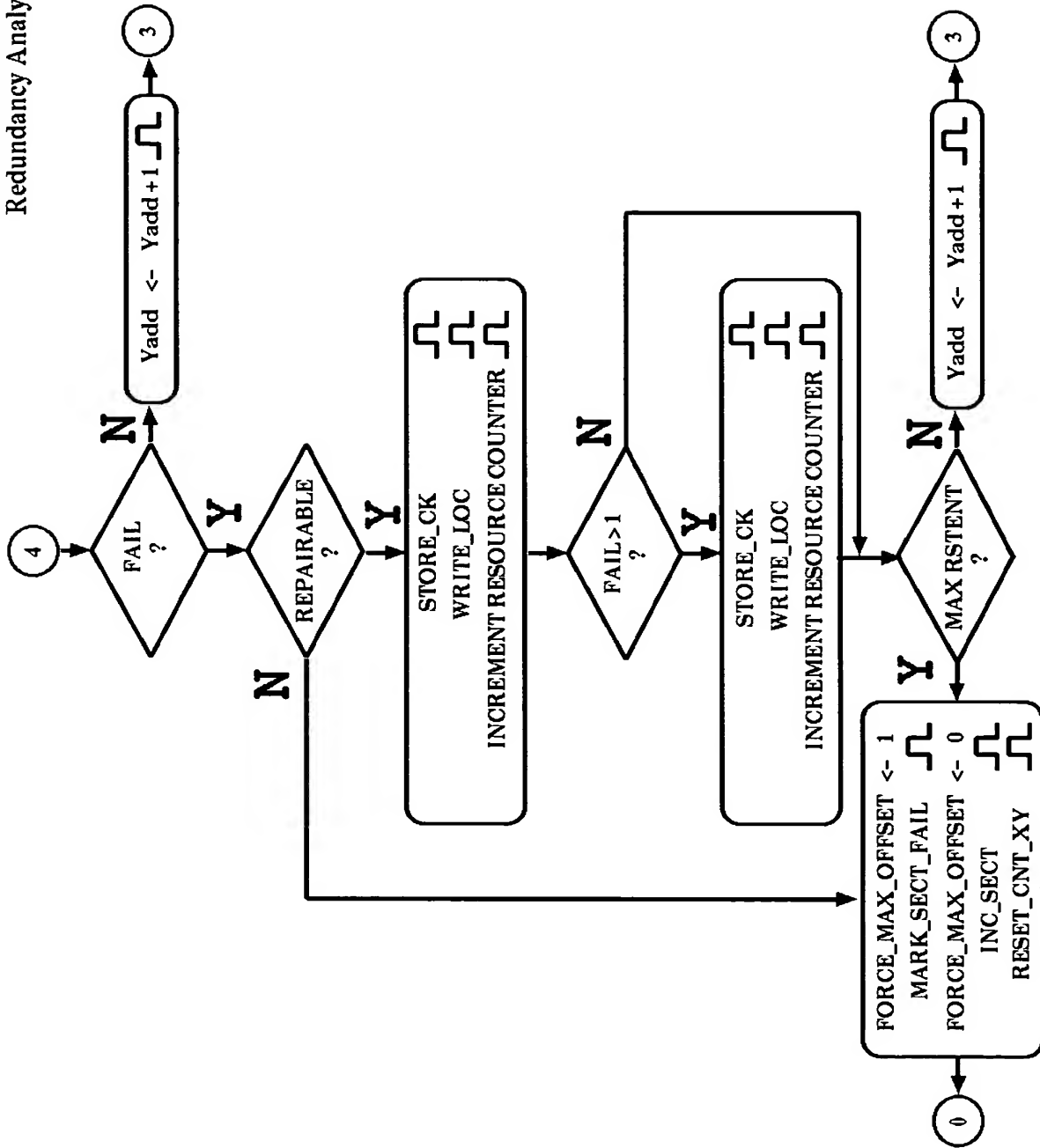


FIG. 7C

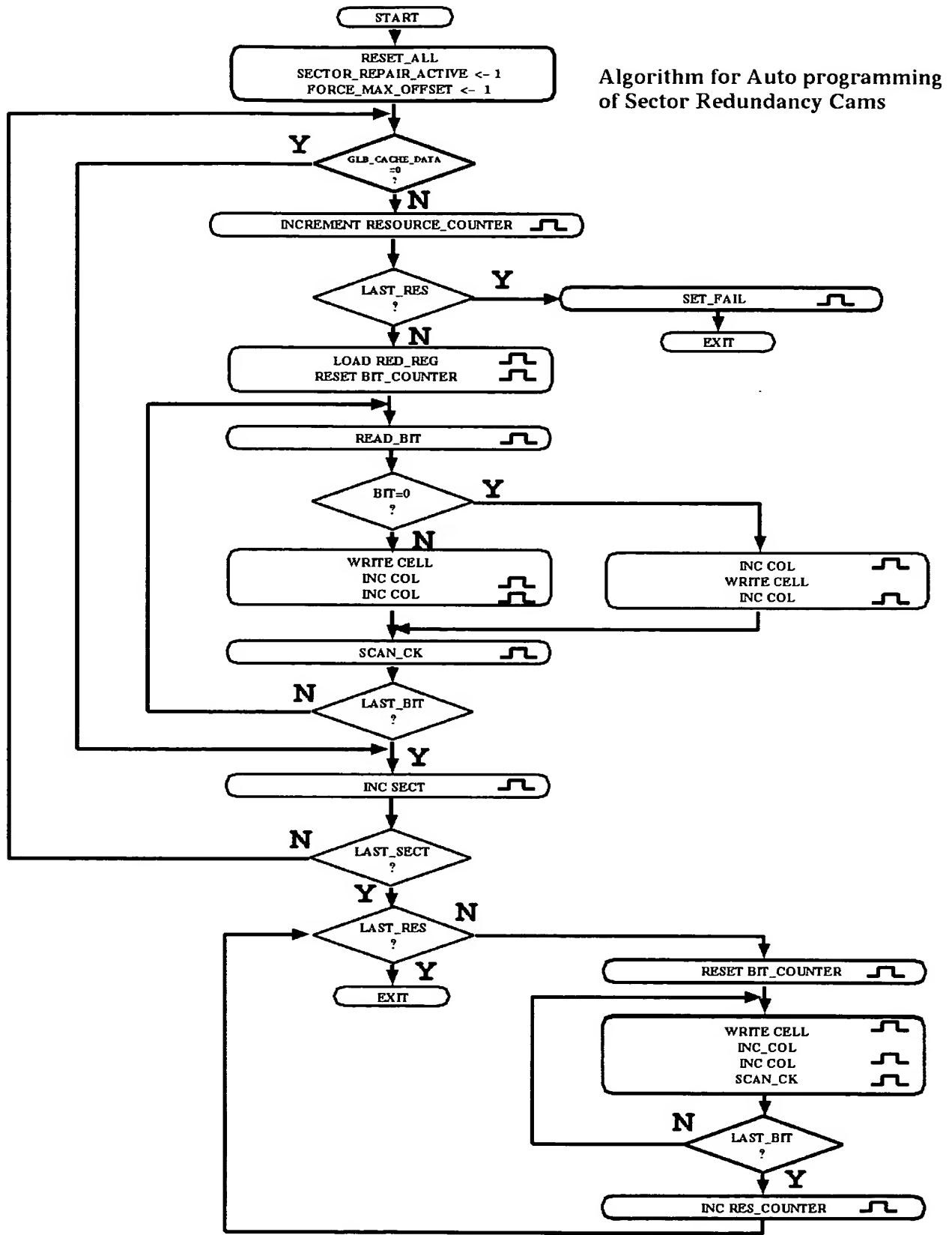


FIG. 8

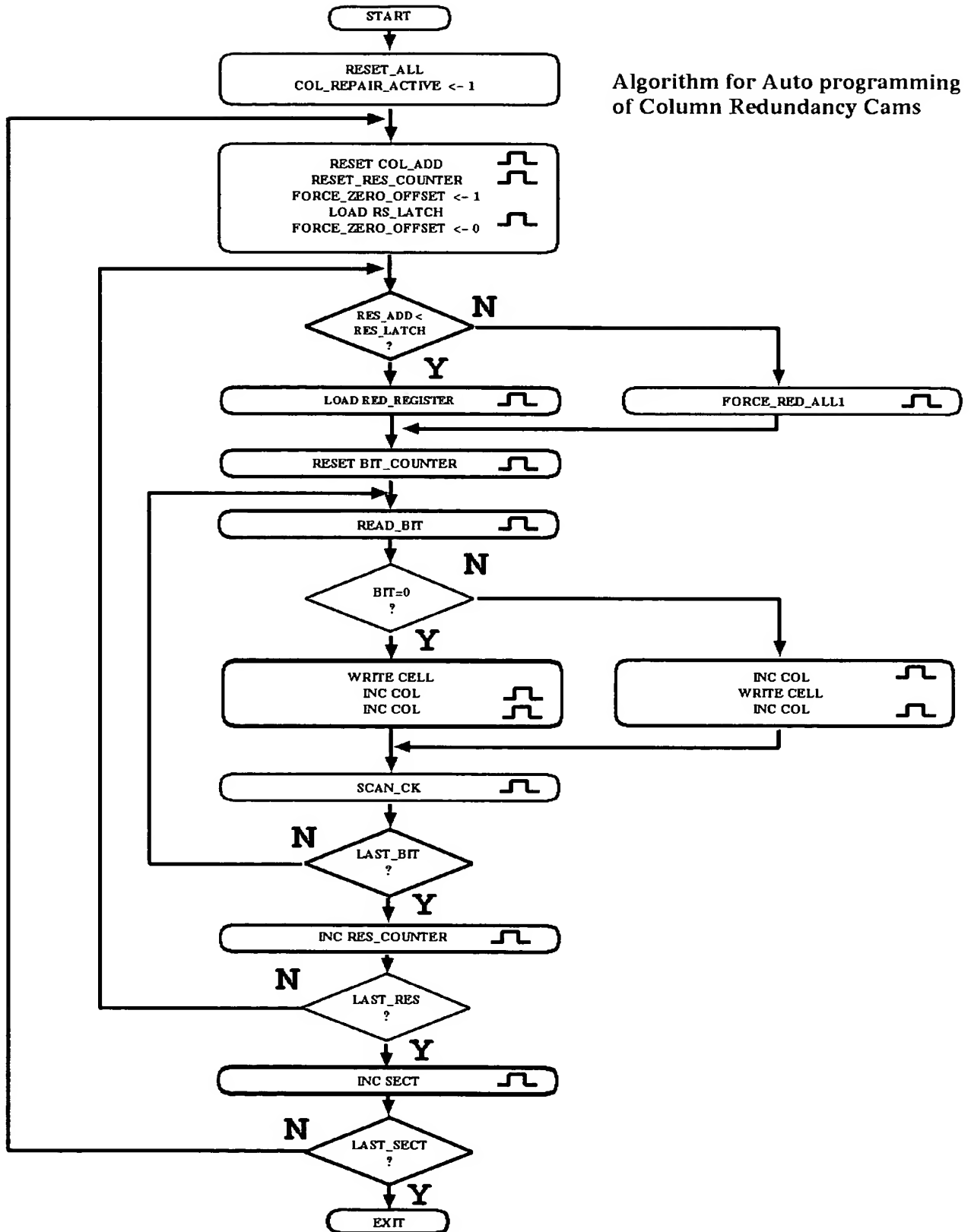


FIG. 9

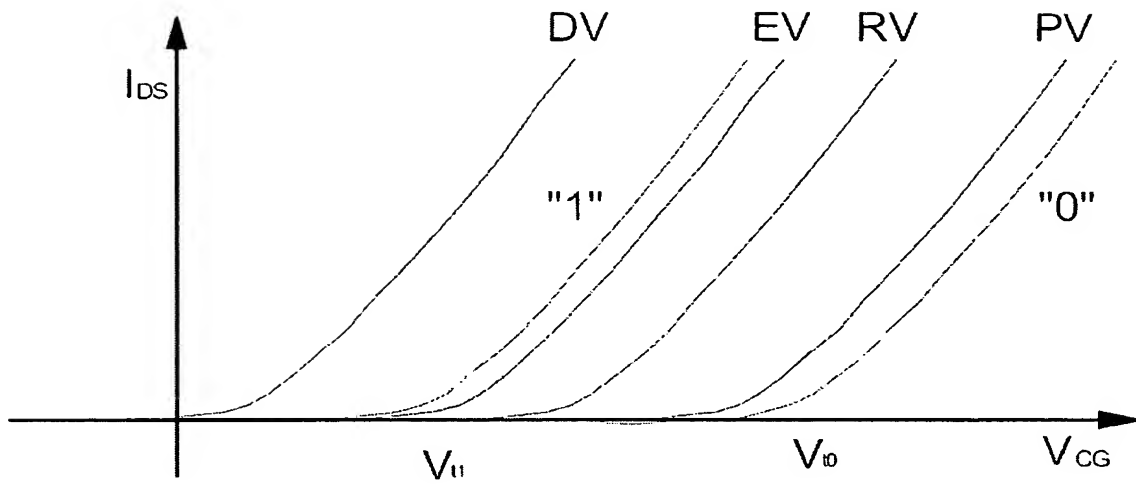


FIG. 10

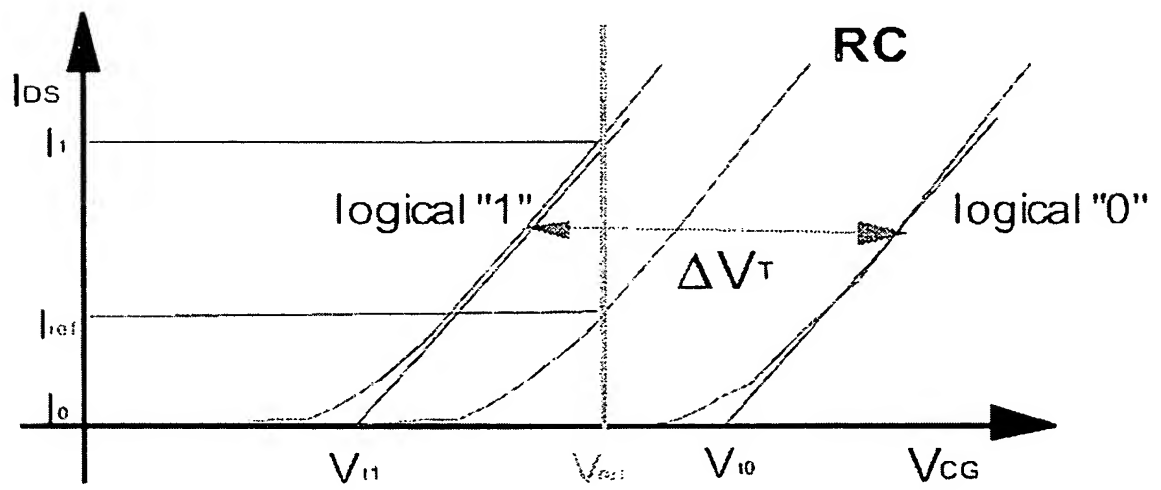


FIG. 11

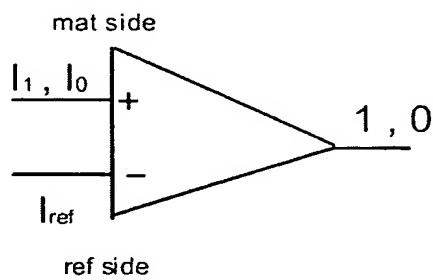
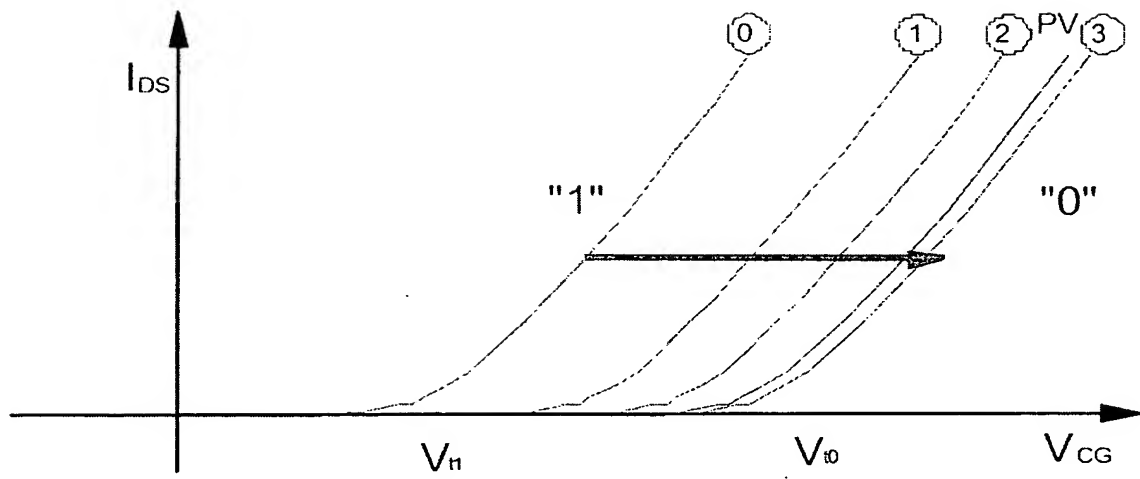
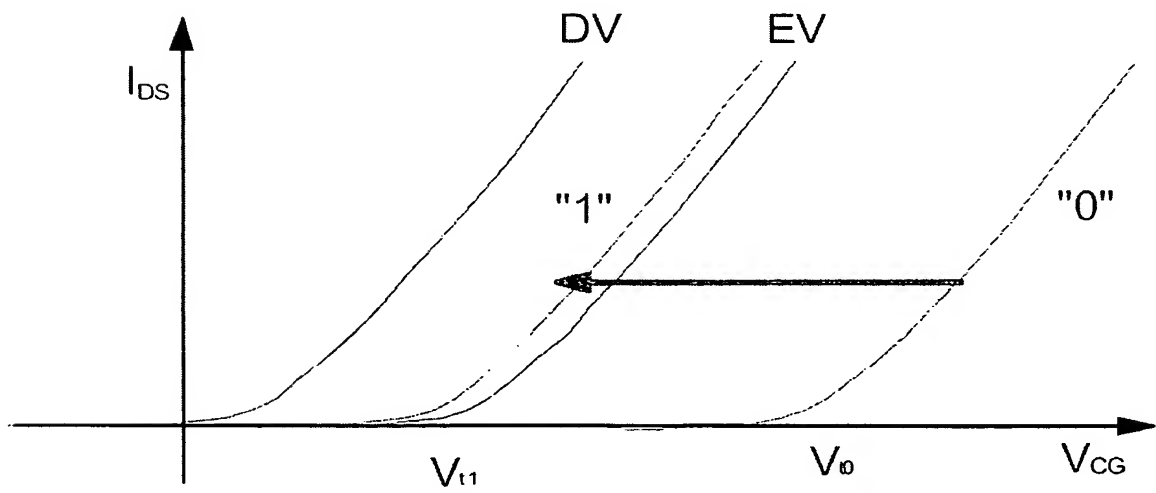
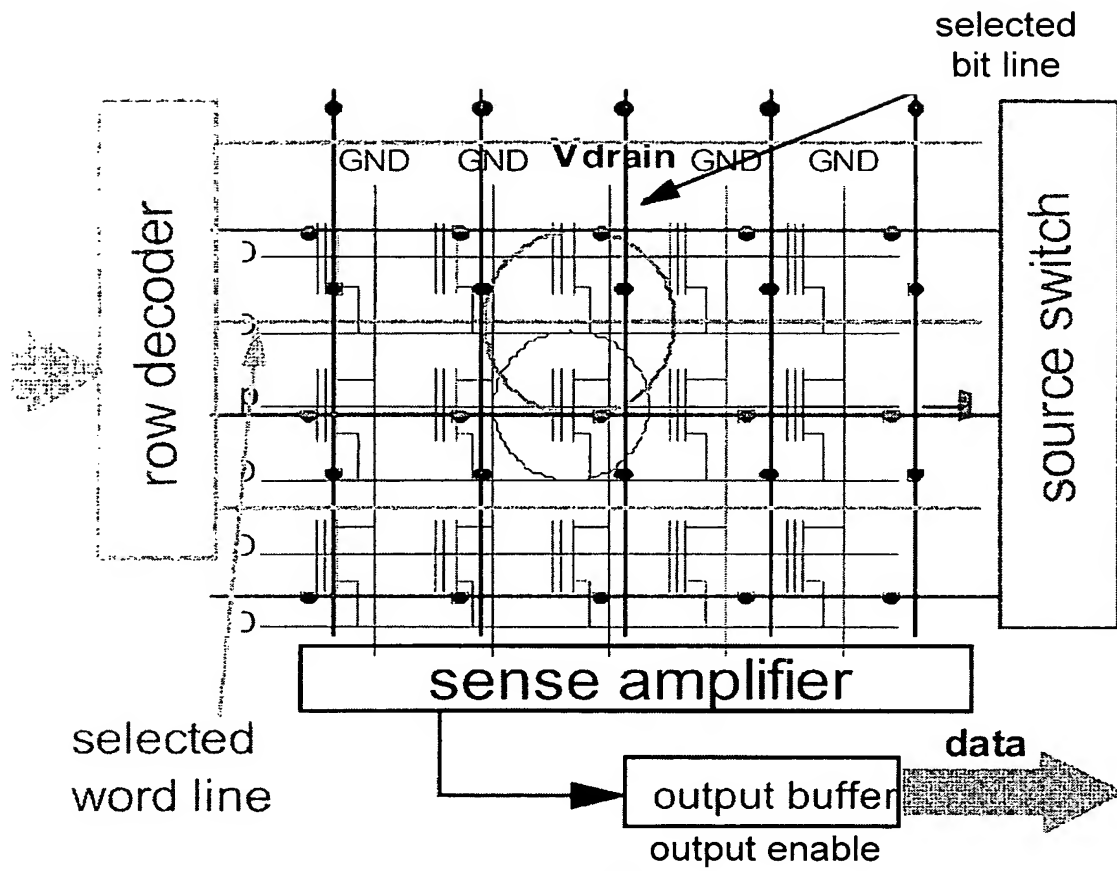
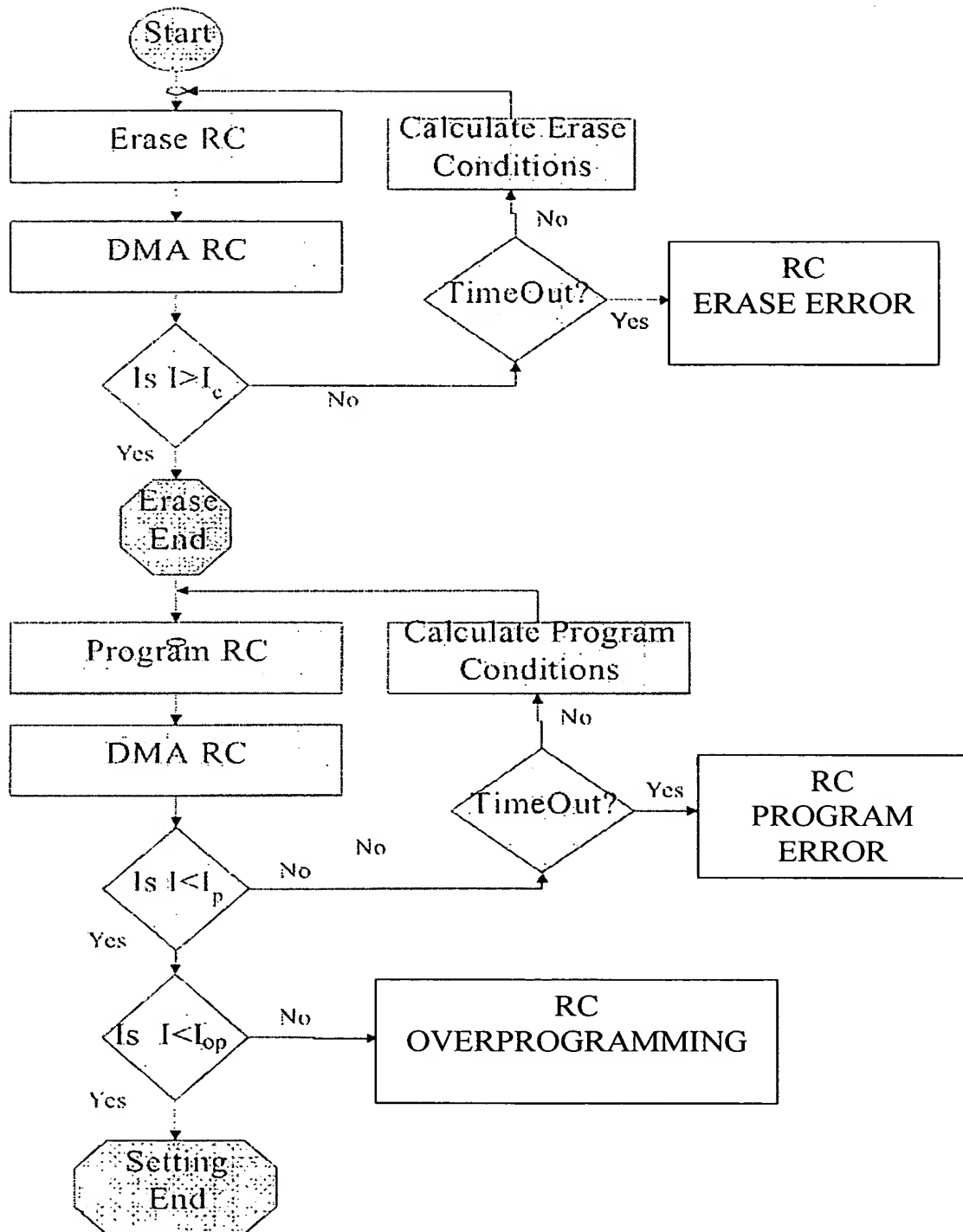


FIG. 12

**FIG. 13****FIG. 14**

**FIG. 15**

**FIG. 16**

Architecture (Part-6)

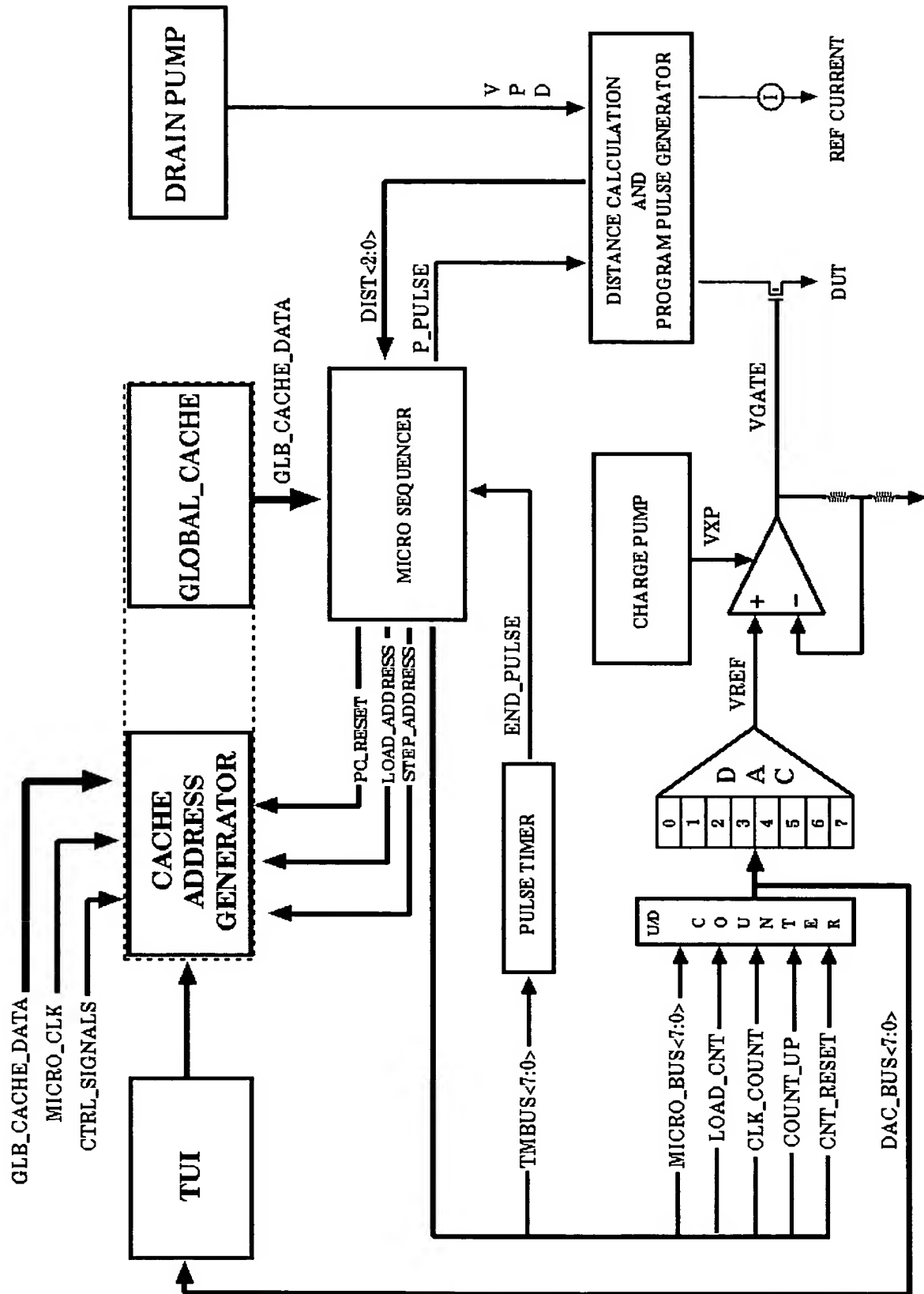


FIG. 17

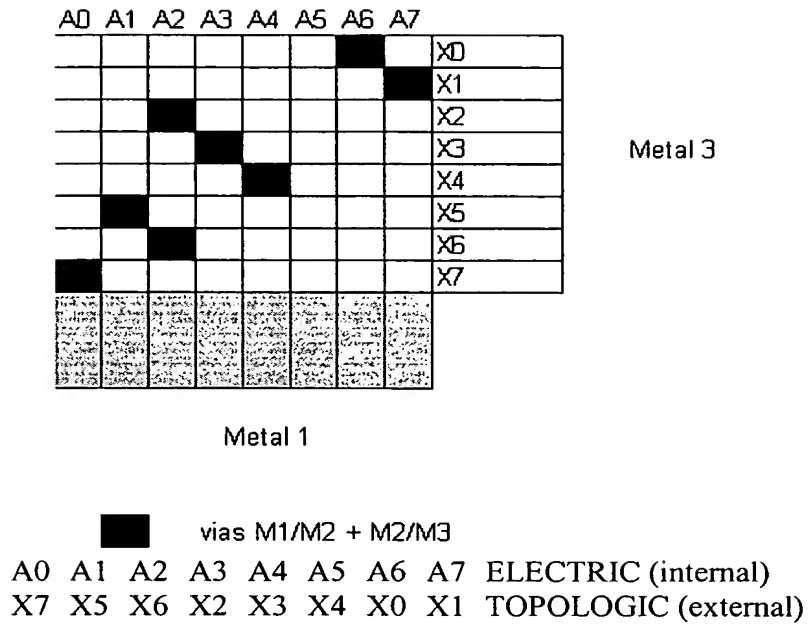


FIG. 18

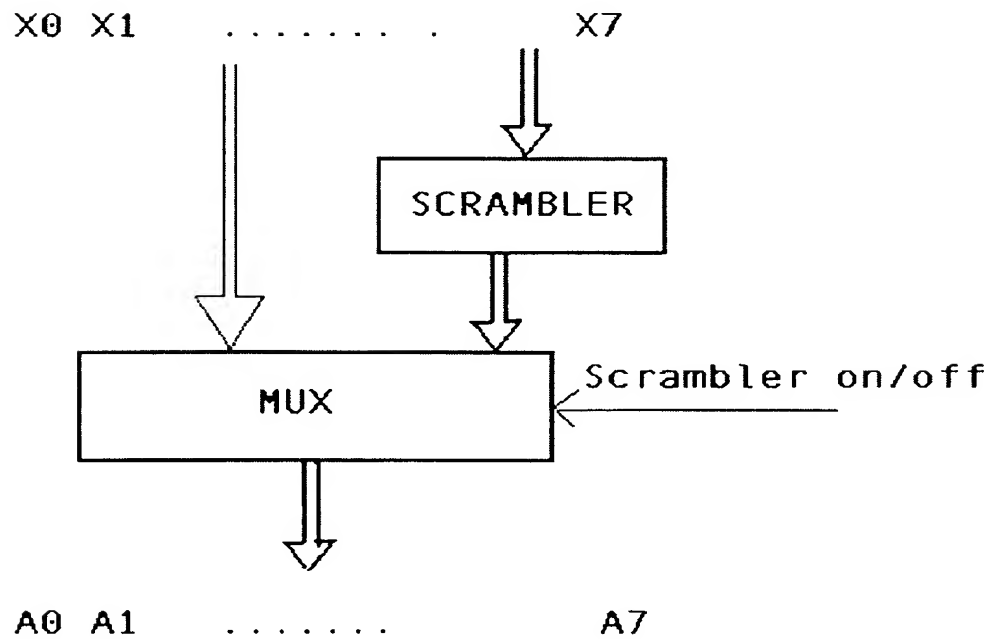


FIG. 19

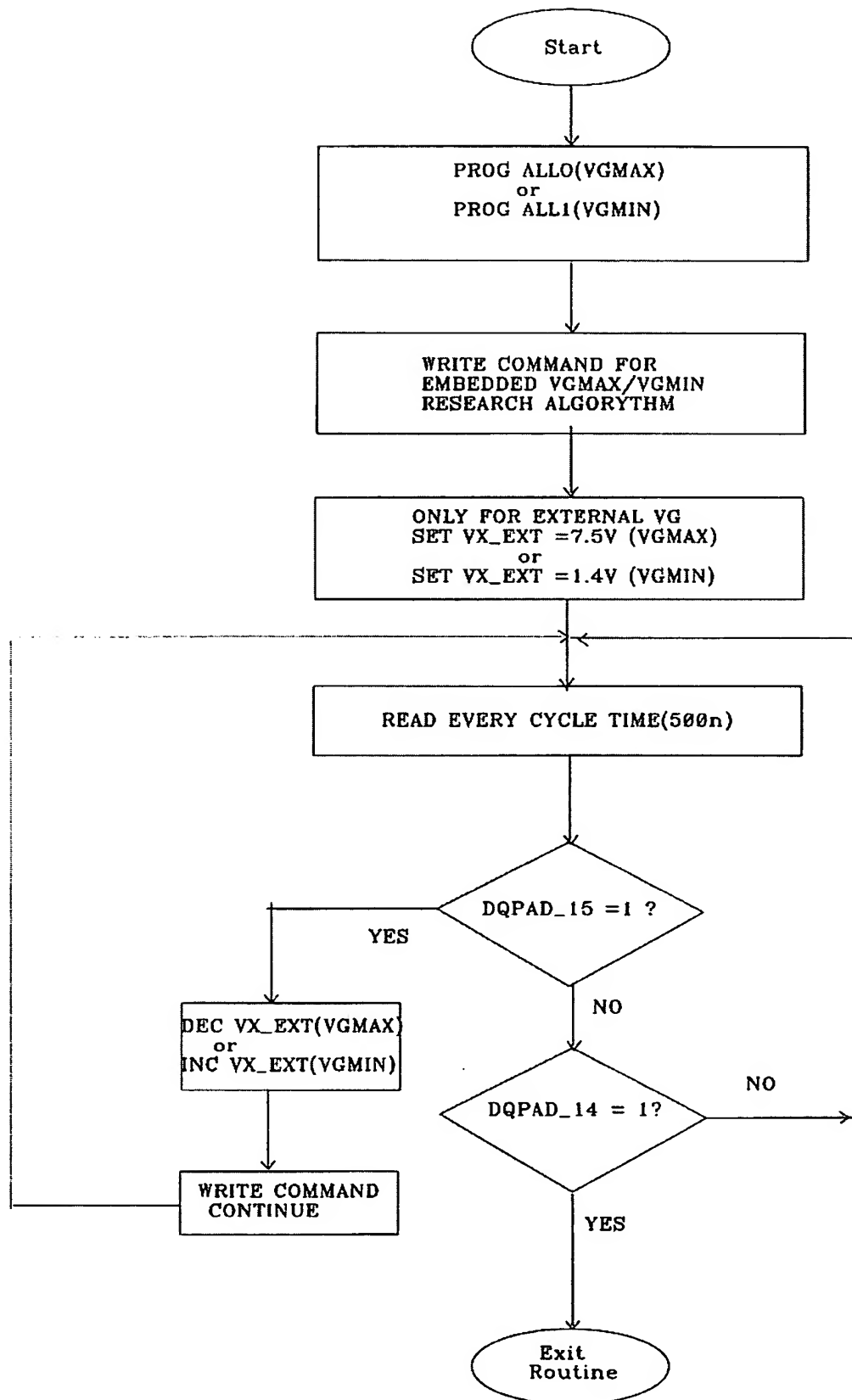


FIG. 20

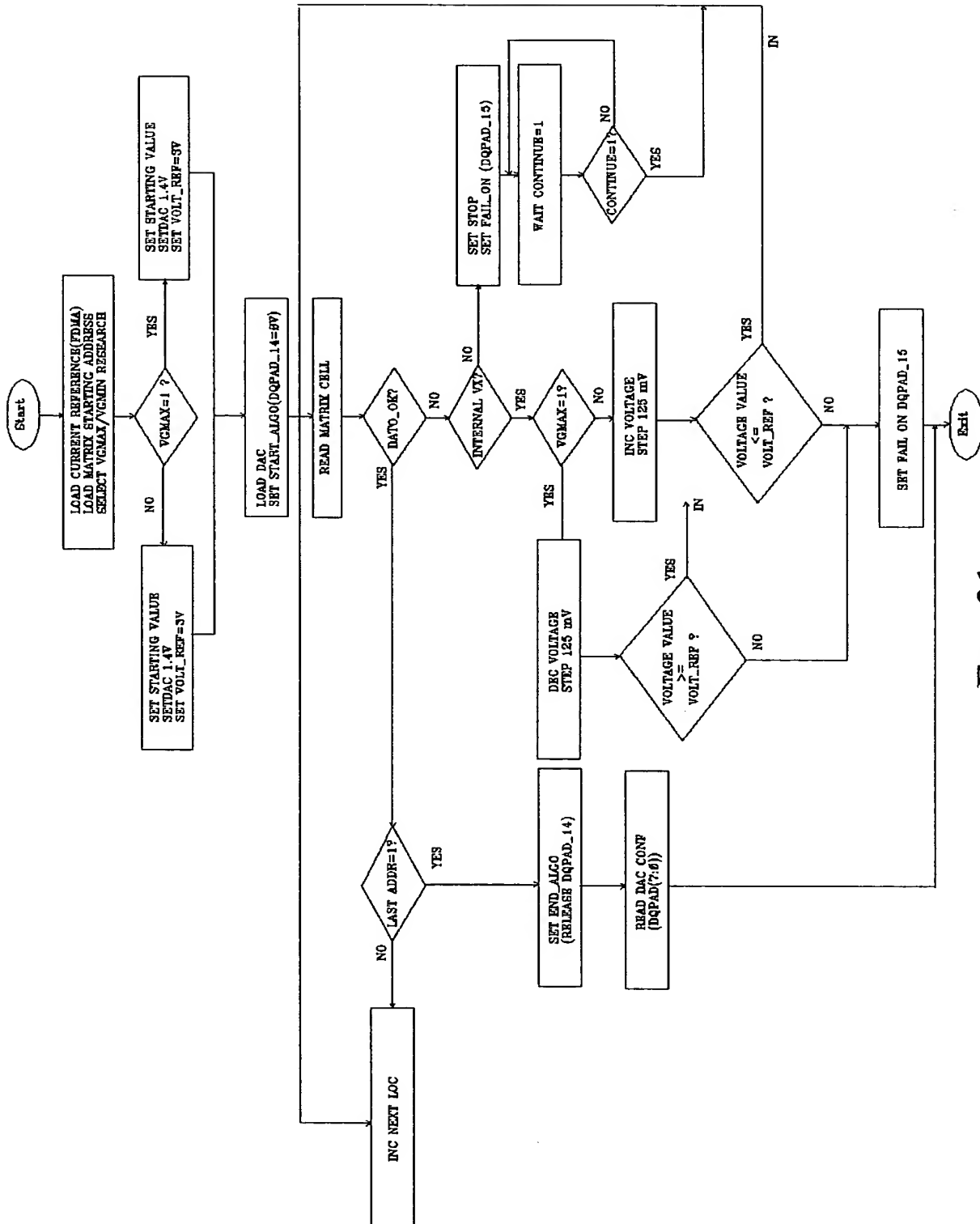


FIG. 21

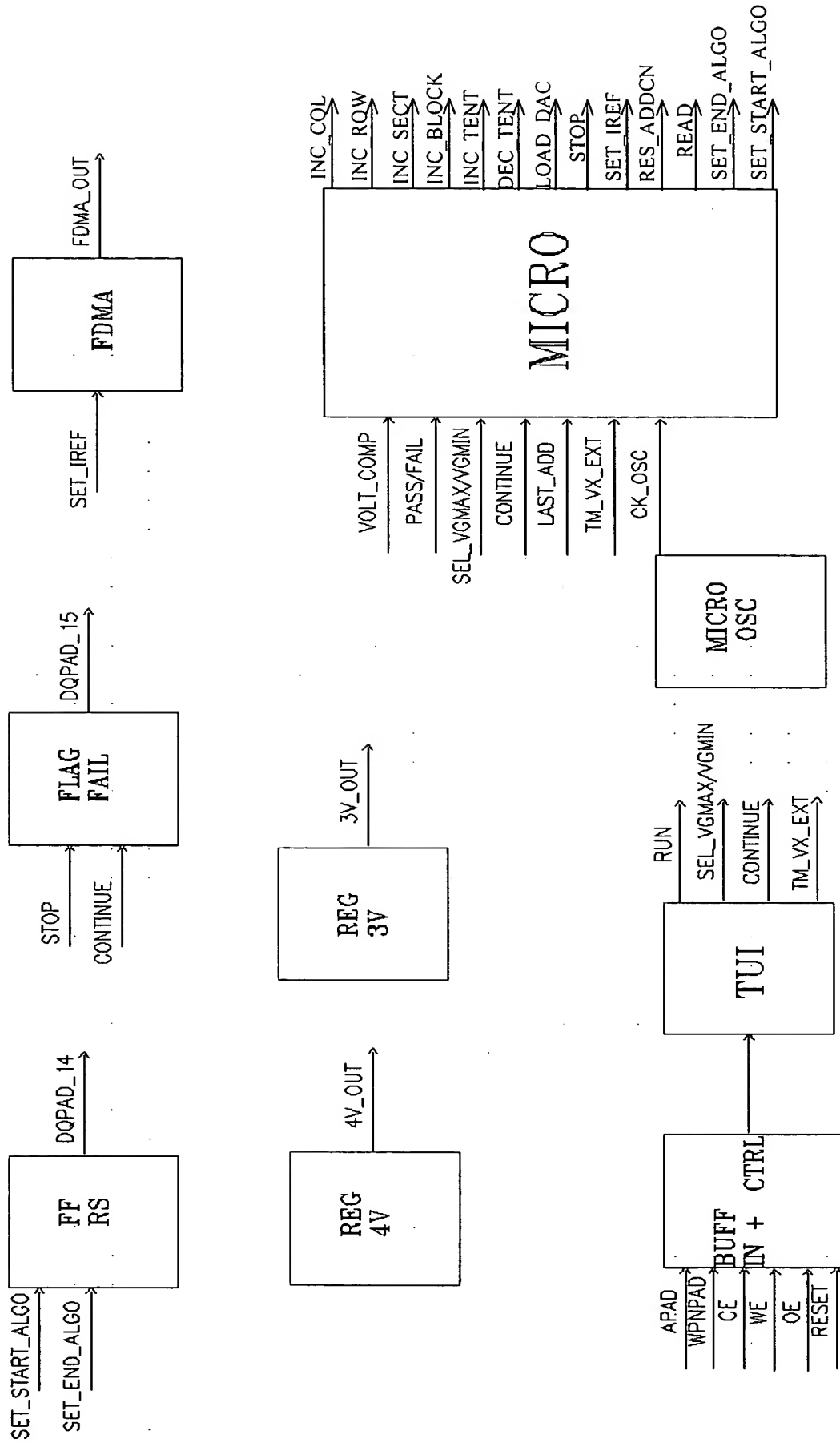


FIG. 22A

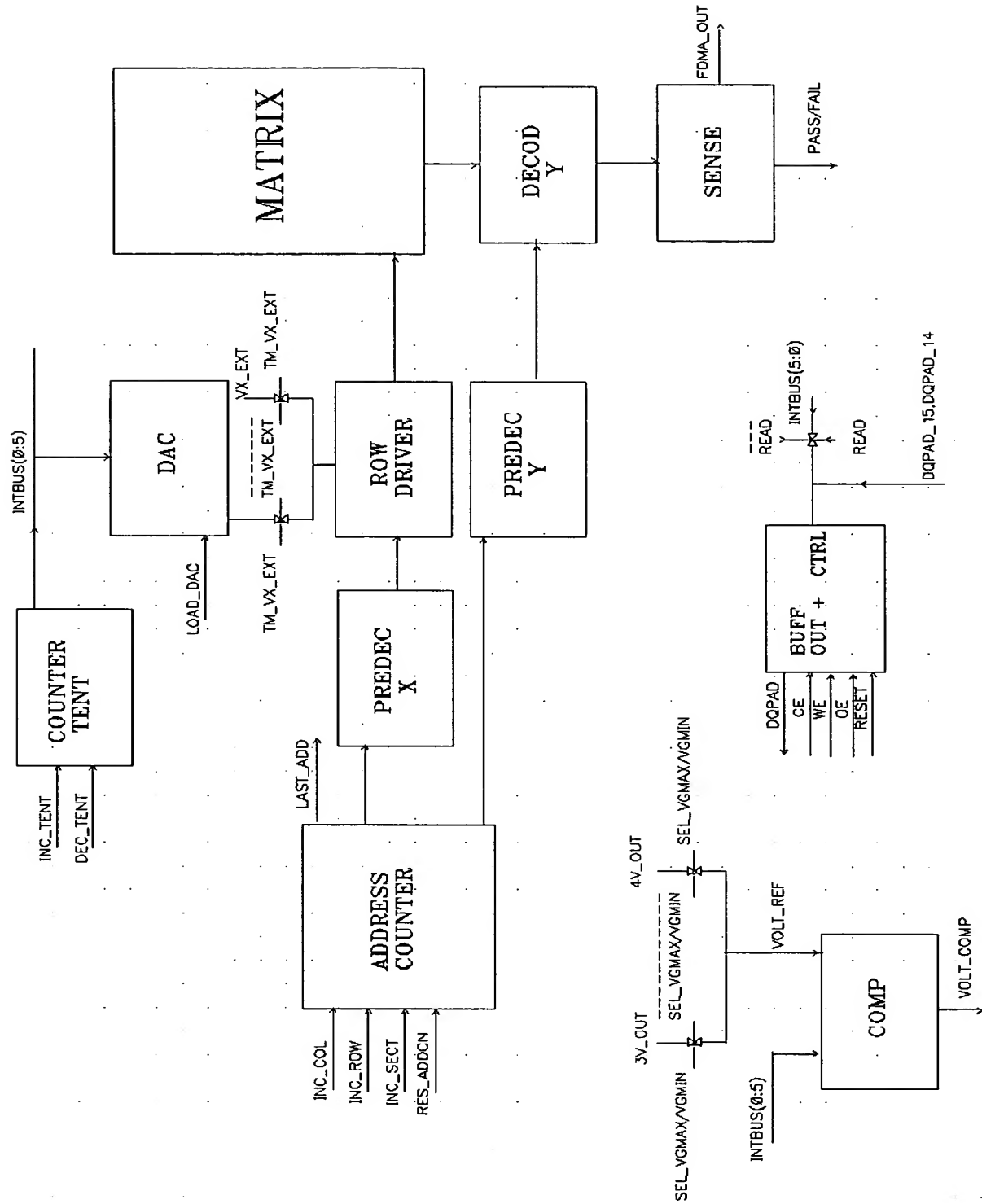
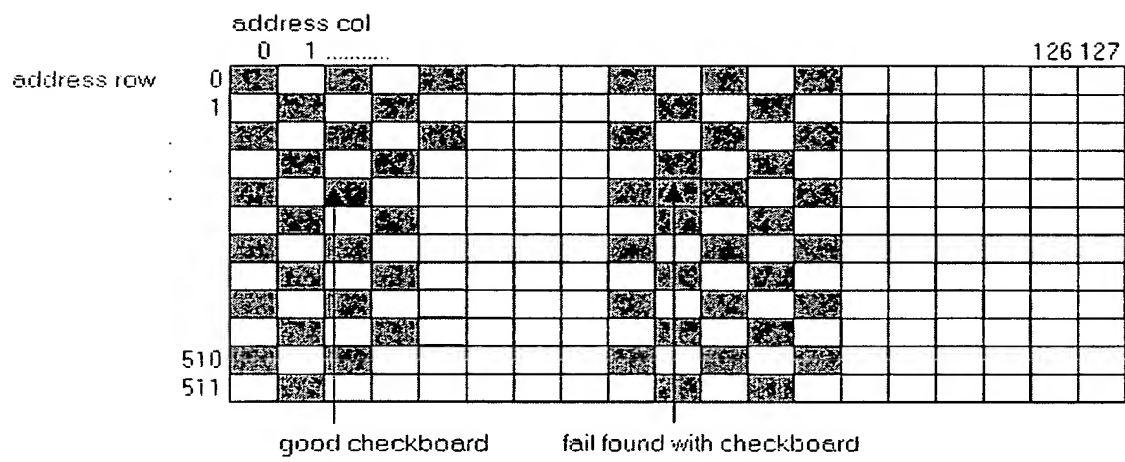


FIG. 22B



OUT 15

FIG. 23

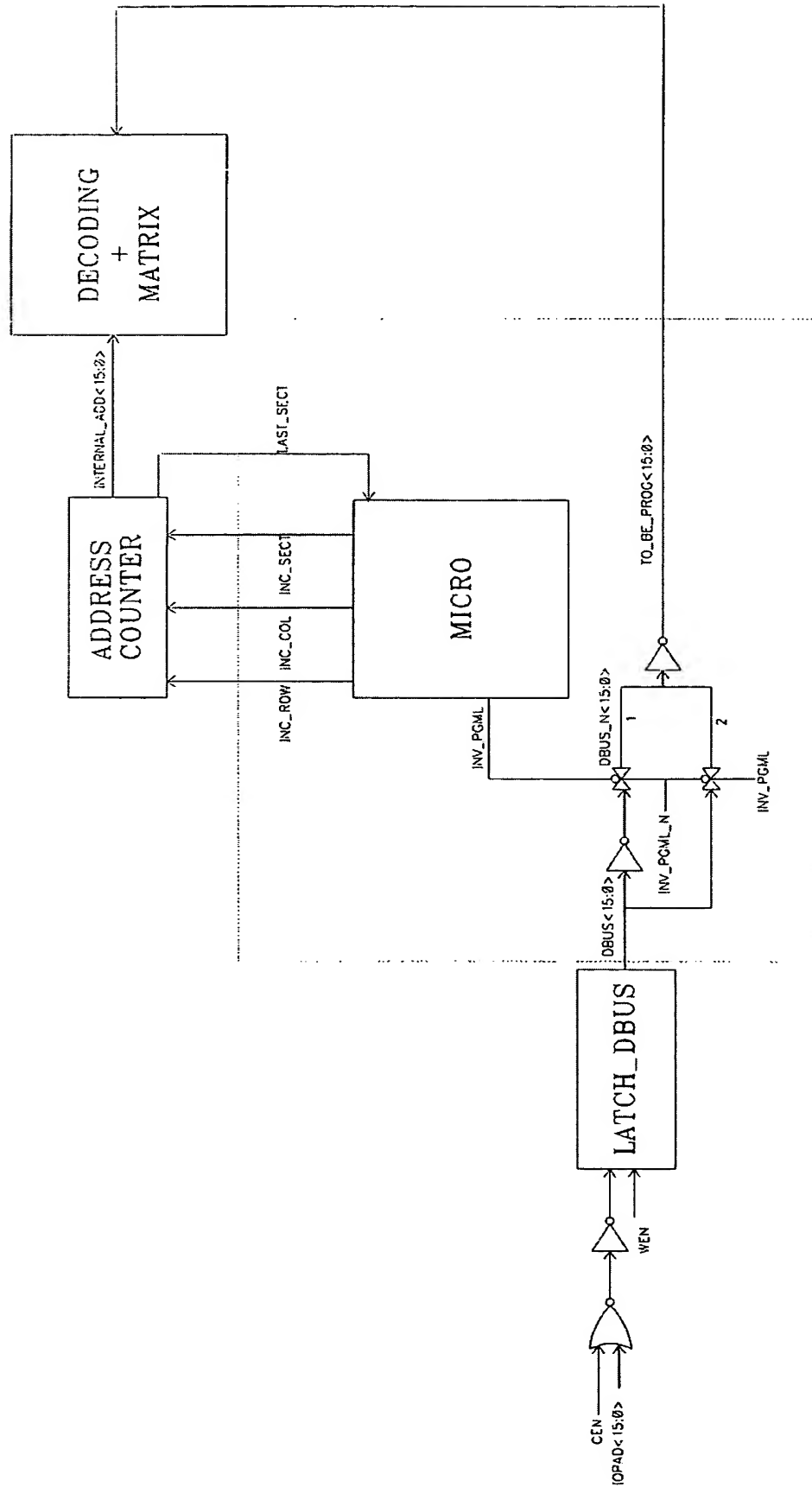


FIG. 24

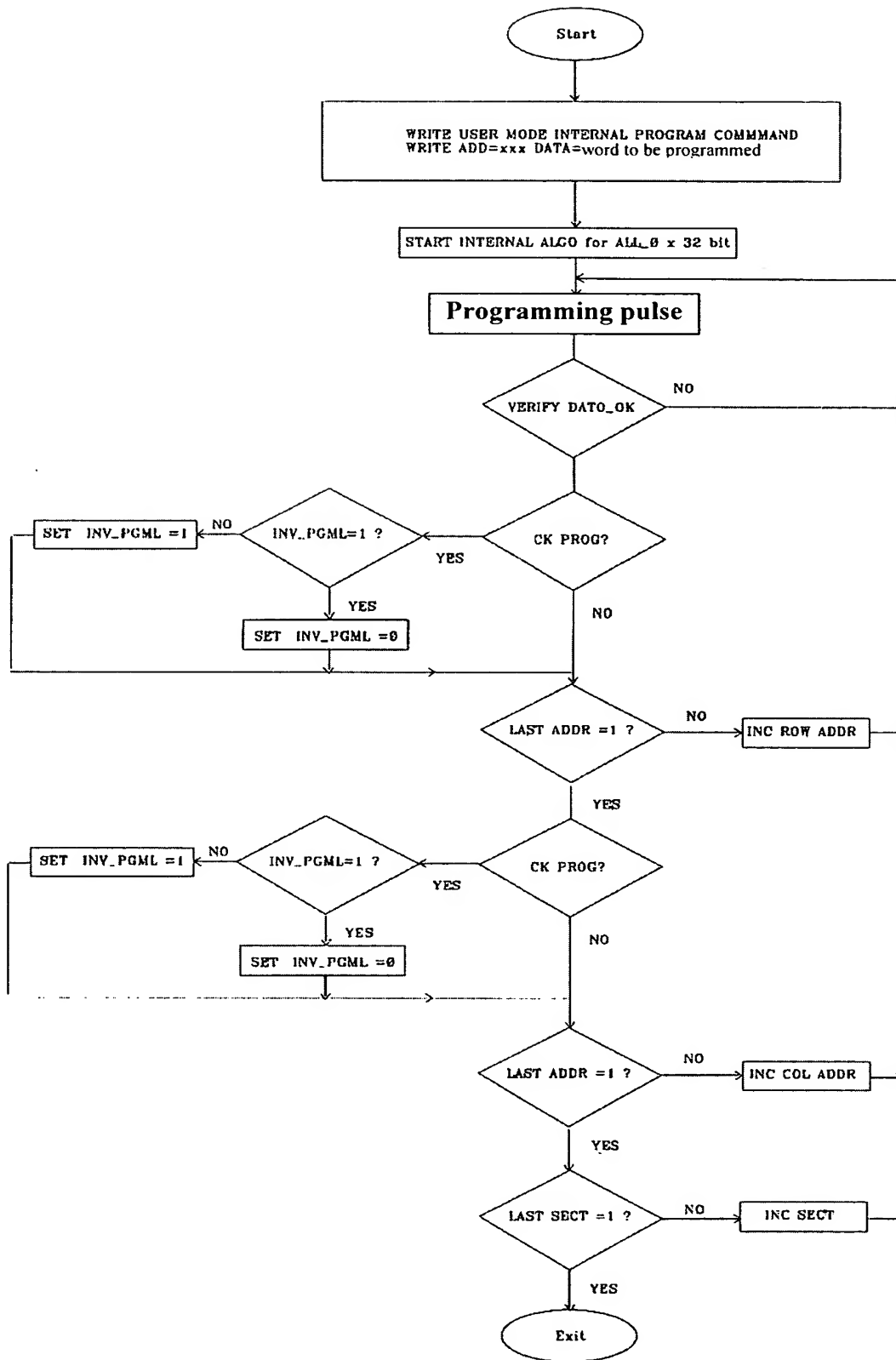


FIG. 25

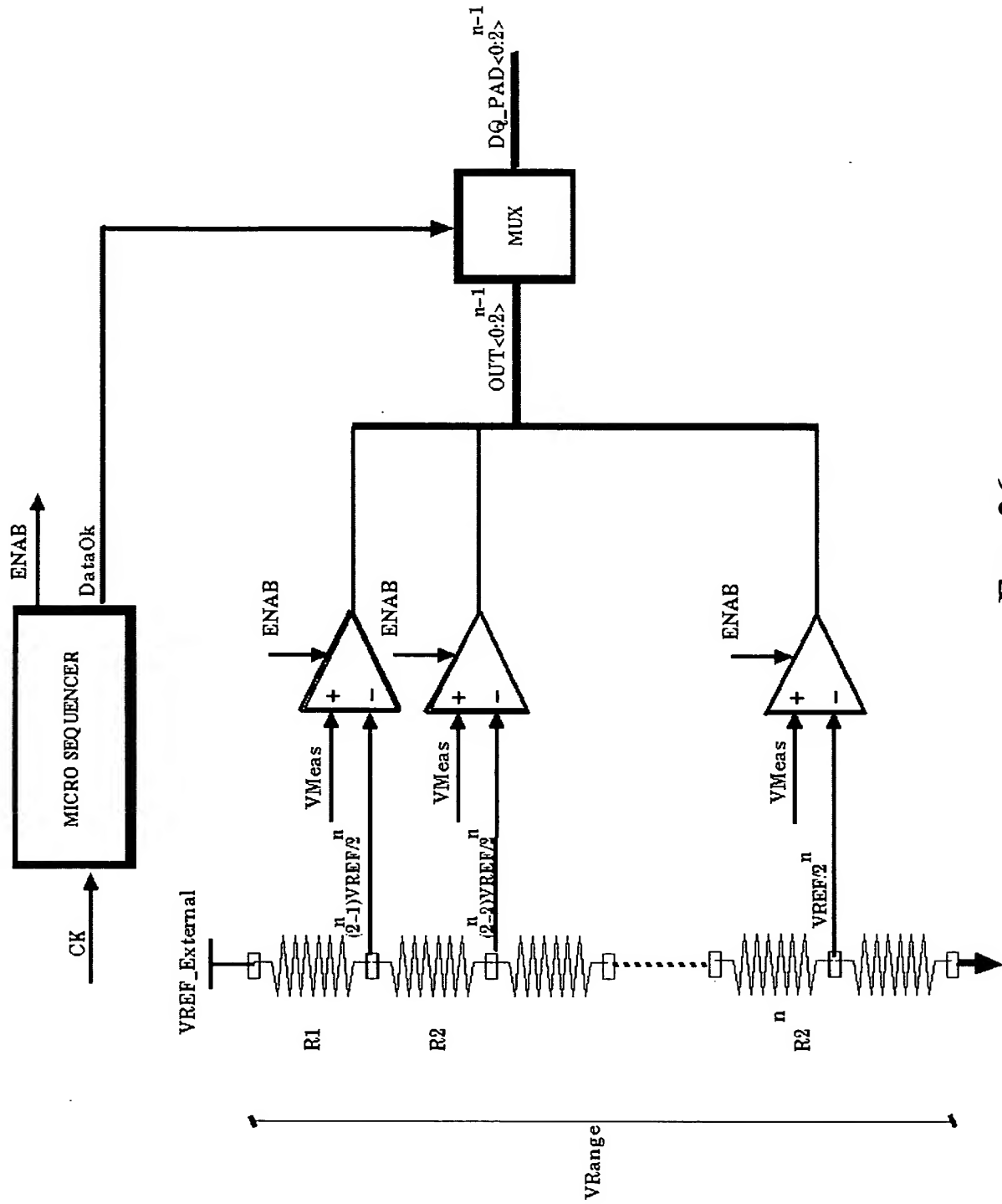


FIG. 26

